



## AMENDMENT TRANSMITTAL LETTER

Attorney Docket  
No:  
N18347102EApplication Serial Number:  
10/600,104Filing Date:  
06/20/2003Examiner:  
Tran, Tan N.Group Art Unit:  
2826

Invention: Semiconductor Integrated Circuit and Method of Manufacturing the Same

TO THE COMMISSIONER OF PATENTS AND TRADEMARKS:

Transmitted herewith is an amendment in the above-identified application. The fee has been calculated as shown below.

## CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	NO. OF EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	16	MINUS	20	0	\$18	\$ 0.00
INDEP. CLAIMS	4	MINUS	6	0	\$84	\$ 0.00

\_\_\_ Petition is hereby made under 37 CFR 1.136(a) to extend the time for response to the Office Action of \_\_\_\_\_ to and through \_\_\_\_\_, comprising an extension of the shortened statutory period of:

\_\_\_ one month (\$110)                      \_\_\_ three months (\$950)  
\_\_\_ two months (\$400)                      \_\_\_ four months (\$1510)

## TOTAL ADDITIONAL FEE FOR THIS AMENDMENT

- \_\_\_ Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted.
- \_\_\_ A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed.
- \_\_\_ A check in the amount of \$ 0.00 is attached.
- \_\_\_ Charge \$ \_\_\_\_\_ to Deposit Account \_\_\_\_\_.
- X No additional fee is required. It is believed that the indicated items are timely filed. However, in the event an extension of time is required, please consider this a petition for extension of time under 37 C.F.R. §1.136(a). Please charge any fees that may be required to Deposit Account No. 50-2087.

March 12, 2004  
date

Darryl Walker  
Darryl Walker  
Reg. No. 43,232

I hereby certify that this correspondence and all correspondence identified as accompanying this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313 on March 12, 2004.

Darryl Walker  
Darryl G. Walker



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Furumiya, Masayuki

Serial No.: 10/600,104

Filed: June 20, 2003

Title: Semiconductor Integrated Circuit and  
Method of Manufacturing the Same

Attorney Docket No.: N18347102E

Group Art Unit: 2826

Examiner: Tran, Tan N.

RESPONSE TO OFFICE ACTION

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

The following is submitted in response to the Office Action dated February 13, 2004,  
and is currently due March 15, 2004.

37 C.F.R. §1.8

I hereby certify that this correspondence is being

[ ] transmitted via facsimile to the United States Patent and Trademark Office to fax number: \_\_\_\_\_  
Date of Transmittal: \_\_\_\_\_

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P.O. Box 1450, Alexandria, VA 22313  
Date of Deposit: \_\_\_\_\_ March 12, 2004 \_\_\_\_\_

Typed/Printed Name: \_\_\_\_\_ Darryl G. Walker \_\_\_\_\_

Signature: Darryl G. Walker